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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10051313	FILING DATE 01/22/2002	CLASS 257	SUBCLASS 1157	GAU 2811	EXAMINER HO, HOA1
**APPLICANTS: Hirano Yasuaki; 365 2818					
**CONTINUING DATA VERIFIED: None / HIR					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-013029 01/22/2001 yes / HIR					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 204552022200	
Verified and Acknowledged Examiners's initials HIR					
TITLE : Nonvolatile semiconductor memory device and method of erasing the same U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims	
Amount Due	Date Paid	Print Claim for O.G.	
		DRAWING	
		Sheets Drawg.	Figs. Drawg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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